

Sheet 1 of 1U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
PU020002SERIAL NO.  
10/  
055,114INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR 1.97

(Use several sheets if necessary)

APPLICANTS  
LOUIS ROBERT LITWIN, JR.  
ET AL.FILING DATE  
January 23, 2002GROUP  
2184

## U.S. PATENT DOCUMENTS

EXAMINE INITIAL		DOCUMENT NUMBER	ISSUE DATE	APPLICANT/PATENTEE	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
EW	AA	6,415,413	07/02/02	W. Pan et al.	714	784	
EW	AB	6,374,383	04/16/02	L. Weng	714	781	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

RECEIVED

MAY 15 2003

Technology Center 2100

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBL. DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION Yes No
	AL						
	AM						
	AN						
	AO						
	AP						
	AQ						

## OTHER INFORMATION (Including Author, Title, Pub.Date, Pertinent Pages, Country, Etc.)

EW	AR	PCT Search Report dated: April 15, 2003.
	AS	
	AT	

EXAMINER

*Emmanuel L. Fide*

DATE CONSIDERED

9/30/04

SUBMITTED BY:

*Guy H. Eriksen*

REG.NO.:

DATE:

GUY H. ERIKSEN

41,736

April 30, 2003

#2

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO. <b>PU020002</b>		SERIAL NO. <i>10/055114</i>	
<b>INFORMATION DISCLOSURE STATEMENT</b> <b>UNDER 37 CFR 1.97</b> (Use several sheets if necessary)				APPLICANTS <b>LOUIS ROBERT LITWIN, JR.</b> <b>ET AL.</b>		FILING DATE <b>01/23/02</b>	
				GROUP <b>2136</b>			
U.S. PATENT DOCUMENTS							
EXAMINE INITIAL		DOCUMENT NUMBER	ISSUE DATE	APPLICANT/PATENTEE	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
<i>EM</i>	AA	4,875,211	10/17/89	K. Murai et al.	<i>714</i>	<i>784</i>	
<i>EM</i>	AB	5,373,511	12/13/94	I. E. Veksler	<i>714</i>	<i>755</i>	
<i>EM</i>	AC	6,058,500	05/02/00	P. A. DesJardins et al.	714	781	
<i>EM</i>	AD	6,061,826	05/09/00	H. Thirumoorthy et al.	714	784	
<i>EM</i>	AE	6,065,149	05/16/00	R. Yamanaka	714	780	
<i>EM</i>	AF	6,175,945	01/16/01	S. Okita	714	784	
<i>EM</i>	AG	6,195,781	02/27/01	S. Kosuge	714	784	
	AH						
	AI						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	PUBL. DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION Yes No
	AL						
	AM						
	AN						
	AO						
	AP						
	AQ						
OTHER INFORMATION (Including Author, Title, Pub.Date, Pertinent Pages, Country, Etc.)							
<i>EM</i>	AR	"Quantum Reed - Solomon Codes", Markus Grassl, Willi Geiselmann, and Thomas Beth, Institut für Algorithmen und Kognitive Systeme, Arbeitsgruppe Quantum Computing, Universität Karlsruhe, Am Fasanengarten 5, 76 128 Karlsruhe, Germany, pp. 1-14.					
<i>EM</i>	AS	"Optimal Finite Field Multipliers for FPGAs", Captain Gregory C. Ahlquist, Brent Nelson, and Michael Rice, article found at <a href="http://splish.ee.byu.edu/docs/ffmult.fpl99.pdf">http://splish.ee.byu.edu/docs/ffmult.fpl99.pdf</a> - Similar pages, 3/1/2001, 10 pages.					
<i>EM</i>	AT	"Tutorial on Reed-Solomon Error Correction Coding", William A. Geisel, NASA Technical Memorandum 102162, August 1990, pp. 72-74.					
EXAMINER <i>Emmanuel / [Signature]</i>				DATE CONSIDERED <i>9-30-04</i>			
SUBMITTED BY: <i>[Signature]</i>				REG. NO.: <b>41,736</b>		DATE: <b>January 23, 2002</b>	
GUY H. ERIKSEN							

10/055114  
01/23/02

#2

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO. <b>PU020002</b>		SERIAL NO. <i>10/055,114</i>	
<b>INFORMATION DISCLOSURE STATEMENT</b> <b>UNDER 37 CFR 1.97</b> (Use several sheets if necessary)				APPLICANTS <b>LOUIS ROBERT LITWIN, JR.</b>			
				ET AL.			
				FILING DATE <b>HEREWITH 01/23/02</b>		GROUP <b>2136</b>	
U.S. PATENT DOCUMENTS							
EXAMINE INITIAL		DOCUMENT NUMBER	ISSUE DATE	APPLICANT/PATENTEE	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	PUBL. DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION Yes No
		AL					
		AM					
		AN					
		AO					
		AP					
		AQ					
OTHER INFORMATION (Including Author, Title, Pub.Date, Pertinent Pages, Country, Etc.)							
<i>EV</i>	AR	"ERROR CONTROL SYSTEMS for Digital Communication and Storage", Stephen B. Wicker, Library of Congress Cataloging-in-Publication Data, p. 209.					
<i>EV</i>	AS	"Parallel Decoder for Cellular Automata Based Byte Error Correcting Code", S. Chattopadhyay and P. Pal Chaudhuri, 10th International Conference on VLSI Design, January 1997, pp. 527-528.					
<i>EV</i>	AT	"Designing a Reed-Solomon Codec in an ADSL System using a C6201 DSP", by Han Kuo, article found, at <a href="http://www.csdmag.com/news/des0111011.htm">http://www.csdmag.com/news/des0111011.htm</a> , 3/9/01, 10 pages.					
EXAMINER <i>Emmanuel F. J. J. J.</i>				DATE CONSIDERED <i>9-30-02</i>			
SUBMITTED BY: <i>Guy H. Eriksen</i>				REG. NO.: <b>41,736</b>		DATE: <b>January 23, 2002</b>	

#2

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE					ATTY. DOCKET NO. <b>PU020002</b>		SERIAL NO. <i>10/055/114</i>	
<b>INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97</b>					APPLICANTS <b>LOUIS ROBERT LITWIN, JR. ET AL.</b>			
(Use several sheets if necessary)					FILING DATE <b>HEREWITH</b> <i>01/23/02</i>		GROUP <i>236</i>	
U.S. PATENT DOCUMENTS								
EXAMINE INITIAL		DOCUMENT NUMBER	ISSUE DATE	APPLICANT/PATENTEE	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE	
	AA							
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	PUBL. DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION Yes No	
	AL							
	AM							
	AN							
	AO							
	AP							
	AQ							
OTHER INFORMATION (Including Author, Title, Pub.Date, Pertinent Pages, Country, Etc.)								
<i>EV</i>	AR	"A Faster Way To Run Reed Solomon Decoders", Torjell Berg and Aaron Brennan, American Microsystems Inc., Article found at <a href="http://www.eetasia.com/article_content.php3?article_id=8800067078">http://www.eetasia.com/article_content.php3?article_id=8800067078</a> 3/9/01; posted: 01 Jan 2001, 3 pages.						
<i>EV</i>	AS	"XF-RSDEC Reed Solomon Decoder", article found at <a href="http://www.xilinx.com/products/logiccore/alliance/memec/xf_rsdec.pdf">http://www.xilinx.com/products/logiccore/alliance/memec/xf_rsdec.pdf</a> , January 10, 2000, pp. 3-1 - 3-5.						
<i>EV</i>	AT	"REED-SOLOMON CODES - 'An introduction to Reed-Solomon codes: principles, architecture and implementation'", article found at <a href="http://www.4i2i.com/reed_solomon_codes.htm">http://www.4i2i.com/reed_solomon_codes.htm</a> , 3/9/01, 8 pages.						
EXAMINER <i>Emanuel L. Wise</i>				DATE CONSIDERED <i>9-30-00</i>				
SUBMITTED BY: <i>Guy H. Eriksen</i>				REG.NO.: <i>41,736</i>		DATE: <i>January 23, 2002</i>		

#2

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE					ATTY. DOCKET NO. <b>PU020002</b>		SERIAL NO. <b>10/055/114</b>	
<b>INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97</b> (Use several sheets if necessary)					APPLICANT <b>LOUIS ROBERT LITWIN, JR. ET AL.</b>			
					FILING DATE <b>01/23/02</b>		GROUP <b>2136</b>	
U.S. PATENT DOCUMENTS								
EXAMINE INITIAL		DOCUMENT NUMBER	ISSUE DATE	APPLICANT/PATENTEE	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE	
	AA							
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	PUBL. DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION Yes No	
	AL							
	AM							
	AN							
	AO							
	AP							
	AQ							
OTHER INFORMATION (Including Author, Title, Pub.Date, Pertinent Pages, Country, Etc.)								
	AR		"Reed Solomon Decoder: TMS320C64x Implementation, 'Digital Signal Processing Solutions'", Jagadeesh Sankaran, TEXAS INSTRUMENTS Application Report SPRA686 - December 2000, pp. 1-20, 31, 38, 49, 60, 67-70.					
	AS							
	AT							
EXAMINER <i>Emmanuel L. Tiso</i>					DATE CONSIDERED <i>9-30-02</i>			
SUBMITTED BY: <i>Guy H. Eriksen</i>					REG.NO.: <b>41,736</b>		DATE: <b>January 23, 2002</b>	